

SELF TIMED BIT AND READ/WRITE PULSE STRETCHERS

ABSTRACT OF THE INVENTION

5 Bit and write decode/drivers, a random access memory (RAM) including the
decode/drivers and an IC with a static RAM (SRAM) including the decode/drivers. The
decode/drivers are clocked by a local clock and each produce access pulses wider than
corresponding clock pulses. The bit decode/driver produces bit select pulses that are
wider than a word select pulse and the write decode/driver produces write pulses that are
10 wider than the bit select pulses for stable self timed RAM write accesses.